



PATENT 1215-0485P(000277-078)

# IN THE U.S. PATENT AND TRADEMARK OFFICE

Applicant:

Alfred P. TURLEY

Conf.:

3492

Appl. No.:

10/648,206

Group:

2811

Filed:

August 27, 2003

Examiner: LOKE, S.H.Y.

For:

BIPOLAR/THIN FILM SOI CMOS STRUCTURE AND

METHOD OF MAKING SAME

## LARGE ENTITY TRANSMITTAL FORM

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 October 1, 2004

#### Sir:

Transmitted herewith is a Reply to Restriction/Election Requirement in the above-identified application.

Requ	irement in the above-identified application.
	The enclosed document is being transmitted via the Certificate of Mailing provisions of 37 C.F.R. § 1.8.
	Petition for ( ) month(s) extension of time pursuant to 37 C.F.R. §§ 1.17 and 1.136(a). \$0.00 for the extension of time.
	No fee is required.
	A check in the amount of \$0.00 is enclosed.
	Please charge Deposit Account No. 02-2448 in the amount of \$0.00. A triplicate copy of this sheet is attached.

If necessary, the Commissioner is hereby authorized in this, concurrent, and future replies, to charge payment or credit any overpayment to Deposit Account No. 02-2448 for any additional fees required under 37 C.F.R. §§1.16 or 1.17; particularly, extension of time fees.

Respectfully submitted,

BIRCH, STEWART, KOLASCH & BIRCH, LLP

WLG/mpe 1215-0485P(000277-078)

Attachment(s)

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# REPLY TO RESTRICTION REQUIREMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 October 1, 2004

# Sir:

In reply to the Restriction Requirement dated September 3, 2004, the following remarks are respectfully submitted in connection with the above-identified application.

This reply includes: Remarks.